

NetFPGA Developers Workshop

August 13-14, 2009 at Stanford University

- You already know that the NetFPGA implements a Gigabit NIC, a hardware-accelerated Internet router, a traffic generator, an OpenFlow switch, a NetFlow probe and more. What else can it do? We invite you, our worldwide NetFPGA Developers, to show off your project. Submit a paper to describe your project, prepare a demo, and come to Stanford in August to demonstrate your work!

- **Papers Due:**

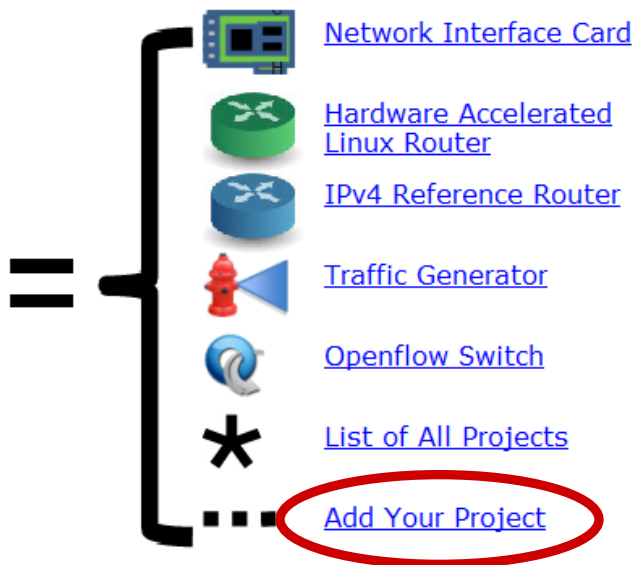
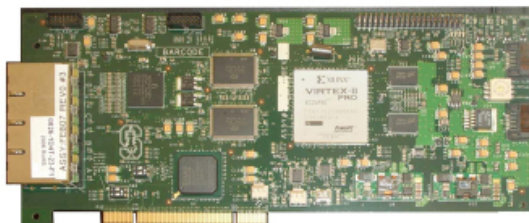
- April 27, 2009

- **Paper Format:**

- 4-8 page, ACM-style

- **Demonstrations:**

- Run on NetFPGA(s)



- **Full Details**

- <http://NetFPGA.org/DevWorkshop>

- **Program Chairs:**

- John W. Lockwood (Stanford University)
- Andrew W. Moore (Cambridge University)

“What have you built with your NetFPGA?”